| IN THE UNITED STATES PATENT AND TRADEMARK OFFICE | |
|---|---|
| In re application of: Geetha Arthanari et al. | Date: 04/17/2008 |
| Serial Number: 10/539,334 | Examiner: Suchin Parihar |
| Filed: 06/15/2005 | Group Art Unit: 2825 |
| Title: ASIC CLOCK FLOOR PLANNING METHOD AND STRUCTURE | IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531 |

AMENDMENT UNDER RULE 312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
OK TO ENTER: /SP/
05/09/2008

Alexandria, VA 22313-1450 05/09/2008 Sir: /SP/

In accordance with 37 CFR 1.312, kindly amend the application as follows.